

Alexis Rodriguez Jr.

701 SW 62nd Blvd - Apt 104 - Gainesville - FL - 32604
Cell: 305-370-8334 Email: a.rod19@gmail.com

<u>Education:</u>	University of Florida M.S. Computer and Electrical Engineering	Gainesville, FL	Current
	University of Florida B.S. Electrical Engineering - Cum Laude	Gainesville, FL	2018
	Miami Dade College A.A. Engineering - Computer	Miami, FL	2013

<u>Projects:</u>	FPGA Networking	Research	Current
	Nallatech 385a Communication	Research	Current
	Glove Controlled Drone 32-bit ARM Cortex (TI MSP432) used to interpret hand gestures via sensors for drone flight, transmit user intended controls to the drone via RF communication, and detect and display communication errors and react accordingly for safety	Design 2	Fall 2017
	32-bit MIPS Emulated Processor Altera Cyclone-III FPGA used to emulate MIPS processor via VHDL	Digital Design	Spring 2017
	Guitar Tuner Microchip PIC18F4620 microcontroller and discrete analog components used to determine correct input frequency via analog filtering and DSP techniques	Design 1	Spring 2017

<u>Employment:</u>	University of Florida - ARC Lab <i>Research Assistant - FPGA</i>	Gainesville, FL	Current
	<ul style="list-style-type: none">❖ Research systems integration of Nallatech 385a FPGA card and its components including the Intel Arria 10 FPGA, Intel's Avalon bus, and PCIe communication via Linux❖ Create partial reconfiguration region for Nallatech 385a for general use in research lab❖ Research cloud and network implementations of FPGAs		
	Intel <i>Programmable Solutions Group Intern</i>	San Jose, CA	Summer 2019/2020
	<ul style="list-style-type: none">❖ Assisted with Agilex Linux driver development❖ ITU G spec testing compliance and characterization for IEEE 1588 on Intel N3000❖ Developed automated tools for ITU network timestamp testing❖ System validation of IEEE 1588 for Wireless 5G technology and communicated need and data across many teams❖ Developed Arduino workshop for hobbyists		

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Texas Instruments

Dallas, TX

Summer 2016/2017

Product & Test Engineering Intern

- ❖ Performed test time reduction (TTR) for a high voltage controller analog device to introduce cost savings of \$50k, code optimization and reduction, and additional test coverage
- ❖ Assisted with ATE conversion of a LDO analog device from legacy hardware to new ATE to introduce cost savings of \$100k and additional test coverage
- ❖ Collected data for customer returns across temperature using ATE and bench equipment

University of Florida

Gainesville, FL Fall 2016/Spring 2017

Teaching Assistant - Electronic Circuits I

- ❖ Instructed lab sections, assisted students with questions or problems with analog circuit designs, and graded student lab reports

TSO Mobile

Doral, FL

2009-2014

Technical Support - Level III

- ❖ Provided customer and technical support to vendors and direct clients
- ❖ Trained new clients and team members on current and to be released products
- ❖ Assisted sales team with high profile bids
- ❖ Performed consultations for new potential clients to determine client needs
- ❖ Performed QA analysis on to be released web-based products
- ❖ Assisted development team with the integration of new GPS hardware and software
- ❖ Developed new algorithms to provide optimized GPS tracking results to servers
- ❖ Redesigned configuration databases to ensure rapid and error-free shipping of devices
- ❖ Developed automated systems to program GPS devices to be sold
- ❖ Designed new hardware to enter new markets
- ❖ Developed server-client software to parse network data for GPS hardware testing

Skills:

Programming: Python, C, C++, Assembly, MATLAB, SQL, VHDL, Verilog, SystemVerilog, MIPS, Visual Basic, Bash, Javascript

Certifications: CCNA, MCSE, CSWA

Software: PSpice, LTSpice, Eagle PCB, Altium Designer, VMWare, TeraTerm, PuTTY, Git, Git Bash, WinSCP, MaaS, Juju, Kubernetes, VIM, Linux, Solidworks, MATLAB, Cadence Allegro & Virtuoso, Quartus Prime Pro, ModelSim, Vivado, Atmel Studio, MPLabX, Eclipse IDE, Code Composer, Visual Studio